ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention	Method of Manufacturing a Semiconductor Device and a Method for Fixing the Semiconductor Device Using Substrate Jig

Submission Type :   Utility Patent Filing

Application Number:

                         10/708067

EFS ID: 54985

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First Named Applicant: Kazuo TESHIROGI

Attorney Docket Number: 020334A

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File Listing:

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us-ids	us-ids.dtd	7763	2004-02-06
us-ids	us-ids.xsl	12026	2004-02-06
us-request	u020334A-usrequ.xml	8618	2004-02-06
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us-request	us-request.xsl	33300	2004-02-06
us-fee-sheet	u020334A-usfees.xml	1766	2004-02-06
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us-fee-sheet	us-fee-sheet.dtd	11069	2004-02-06
application-body	spec2-trans.xml	98407	2004-02-06
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application-body	application-body.dtd	49498	2004-02-06
application-body	wipo.ent	4956	2004-02-06
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application-body	isoamsr.ent	7073	2004-02-06
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application-body	mmlalias.ent	38209	2004-02-06
application-body	soextblx.dtd	12870	2004-02-06
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package-data	u020334A-pkda.xml	10982	2004-02-06
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package-data	us-package-data.xsl	19263	2004-02-06
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